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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/727,760	12/03/2003	Jim Mali	ARTCP050	7568
25920	7590 04/14/2005		EXAMINER	
MARTINE PENILLA & GENCARELLA, LLP 710 LAKEWAY DRIVE			NGUYEN, THINH T	
SUITE 200	AY DRIVE		ART UNIT	PAPER NUMBER
SUNNYVALE, CA 94085			2818	<u>.</u>
			DATE MAILED: 04/14/2009	5

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	((8			
Office Astion Comment		10/727,760	MALI ET AL.				
	Office Action Summary	Examiner	Art Unit				
		Thinh T. Nguyen	2818				
Period fo	The MAILING DATE of this communicati or Reply	on appears on the cover sheet v	vith the correspondence addr	ess			
THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICAT nsions of time may be available under the provisions of 37 SIX (6) MONTHS from the mailing date of this communicate period for reply specified above is less than thirty (30) day period for reply is specified above, the maximum statutory reto reply within the set or extended period for reply will, be reply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	FION. CFR 1.136(a). In no event, however, may a tion. is, a reply within the statutory minimum of the period will apply and will expire SIX (6) MC by statute, cause the application to become A	a reply be timely filed irty (30) days will be considered timely. DNTHS from the mailing date of this comm ABANDONED (35 U.S.C. § 133).	munication.			
Status							
1)⊠	Responsive to communication(s) filed or	n <u>14 March 2005</u> .					
2a)	This action is FINAL . 2b)	☐ This action is non-final.	•				
3)	3) Since this application is in condition for allowance except for formal matters, prosecution as to the ments is						
	closed in accordance with the practice u	nder <i>Ex parte Quayle</i> , 1935 C.	D. 11, 453 O.G. 213.				
Disposit	ion of Claims		4				
5)□ 6)⊠ 7)⊠	Claim(s) 1,2 and 4-10 is/are pending in t 4a) Of the above claim(s) is/are w Claim(s) is/are allowed. Claim(s) 1,2 and 4-9 is/are rejected. Claim(s) 10 is/are objected to. Claim(s) are subject to restriction	ithdrawn from consideration.		,			
Applicati	ion Papers						
10)⊠	The specification is objected to by the Ex The drawing(s) filed on is/are: a)[Applicant may not request that any objection Replacement drawing sheet(s) including the The oath or declaration is objected to by	☐ accepted or b) ☐ objected to to the drawing(s) be held in abeya correction is required if the drawin	ance. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR	, ,			
Priority u	ınder 35 U.S.C. § 119						
12) [a)	Acknowledgment is made of a claim for for All b) Some * c) None of: 1. Certified copies of the priority documents of the priority documents. Copies of the certified copies of the application from the International Eace the attached detailed Office action for	uments have been received. uments have been received in a re priority documents have been Bureau (PCT Rule 17.2(a)).	Application No n received in this National St	age			
2) 🔲 Notic 3) 🔲 Inform	t(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-9 mation Disclosure Statement(s) (PTO-1449 or PTO/ r No(s)/Mail Date	48) Paper No	Summary (PTO-413) o(s)/Mail Date Informal Patent Application (PTO-1	52)			

DETAILED OFFICE ACTION

1. Applicant election of claim 1-2,4-10 for prosecution of the present application without traverse in the communication with the office on 3/14/2005 is acknowledged.

Drawings

2. Fig. 2,3, and 4 are not designated by a legend such as "prior art". The legend is. necessary in order to clarify what applicant 's invention is (see MPEP paragraph 608.02). Correction is required.

Specification

3. The specification is objected to for failing to enable the invention and make it non-compliant with 35 U.S.C. 112 first paragraph.

Applicant discloses in paragraph [0012] that the n-type transistor is fabricated on an n-type substrate and the p-type transistor is fabricated on a p type substrate. This disclosure is different from a conventional well known structure as shown in fig 4 in US patent 5,843,816 by Liaw that shows that N-type is fabricate on P-well (or P substrate) and P-type is fabricate on N-well (or N-substrate); also see fig 4-2 (a) page 137 From Silicon Processing for the VLSI Era (volume 3) by Stanley Wolf published by Lattice press (1995) attached to the PTO-892 of this Office Action.

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Can the Applicants give evidence of prior art that have a P-type transistor (a MOSFET transistor that the source and drain are P-type) that have a P-channel built on a P substrate or a N-type transistor (a MOSFET transistor that the source and drain are N-type) that have a N-channel built on a N substrate with a n-channel since the Examiner is not aware of those structure.

Correction or clarification is required.

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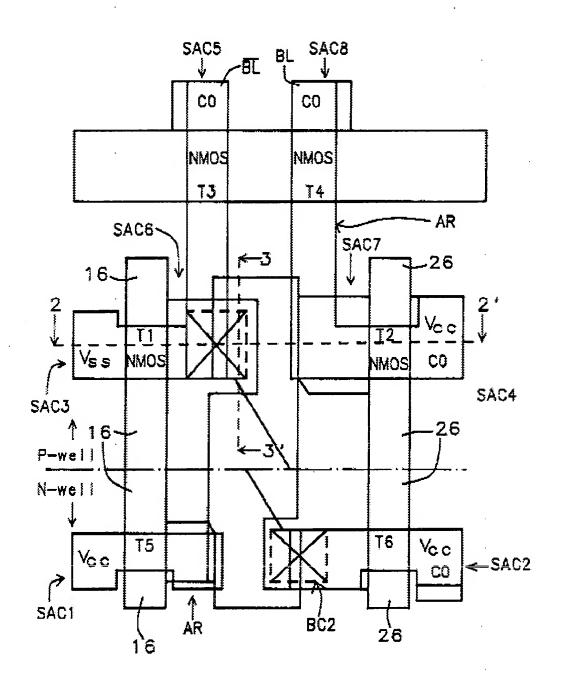


FIG. 4

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4. The specification has been checked to the extent necessary to determine the presence of all possible minor errors. However, the applicant cooperation is requested in correcting any errors of which the applicant may become aware in the specification.

Claims Objections

5. Claims 6,7,8 are all objected for being not-enabling and make them non-compliant with 35 U.S.C. 112 first paragraph. There are not enough disclosures in the specification or the drawings for the claimed p-transistor on a p substrate or a n-transistor on a n-substrate so that a person skilled in the art can make use of these inventions.

Claim Rejections - 35 USC § 102

- 6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102(b/e) that form the basis for the rejections under this section made in this office action.
 - (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
 - (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 7. Claims 1,4,5 are rejected under 35 U.S.C. 102(e) as being anticipated by Kumagai et al.

 (US patent 6,657,243)

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REGARDING CLAIM 1,4

Kumagai (the abstract, fig 17, fig 19) discloses a core cell (a SRAM cell) fabricated upon a substrate of three columns of substrate material, the three columns of substrate material comprising a first n-type column, a first p-type column and a second n-type column, the p-type column being positioned between the first and second n-type columns and a random access memory comprised of at least a plurality of core cells, a core cell, comprising:

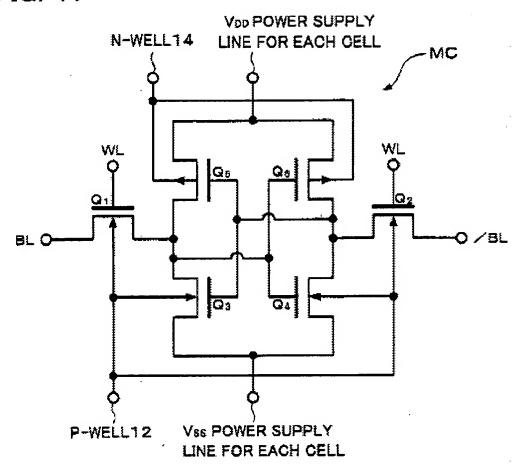
first n-type substrate; first p-type substrate; and second n-type substrate, the first and second n-type substrate being positioned on opposite sides of the first p-type substrate.

REGARDING CLAIM 5

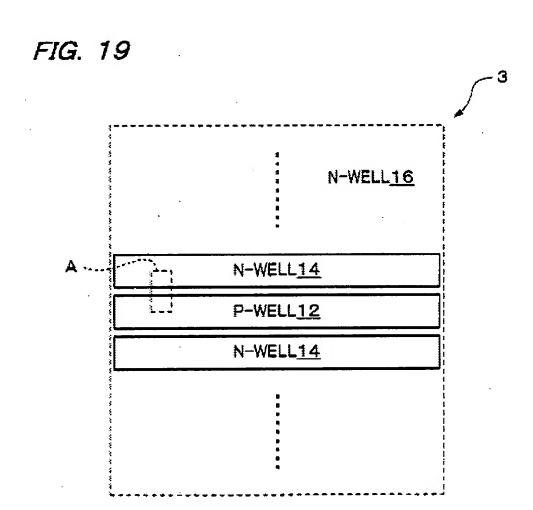
Kumagai disclose (in fig 19) a core cell wherein the substrates have rectangular shapes.

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FIG. 17



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Claim Rejections - 35 USC § 103

8. The following is a quotation of U.S.C. 103(a) which form the basis for all obviousness rejections set forth in this office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains.

Patentability shall not be negatived by the manner in which the invention was made.

9. Claim 6,7,8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Wang (US patent 6,621,125) in view of further remark.

To expedite prosecution of the application the examiner assume that the Applicant will amend claim 6,7,8 to make them in compliant with 35 U.S.C. 112 first paragraph and overcome the objection by the examiner.

REGARDING CLAIM 6,7

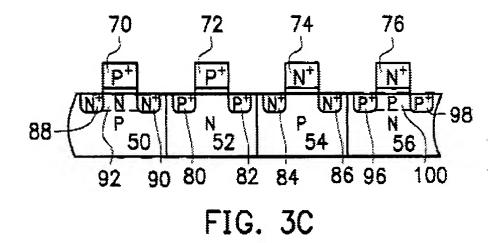
Wang disclose a first and second n-type (layer 52 and 56 in fig 3C with a p type (layer 54) in middle (fig 3C) and his structure are intended for use in SRAM fabrication. (column 1 line 20)

REGARDING CLAIM 8

The couplings of the transistors to make a core cells is old and well known in the art as evidenced by the disclosure in fig 1 by Liaw et al. (US patent 5,843,816).

A person skilled in the art at the time the invention was made would have been able to come up with the invention of claim 8 using the disclosure by Wang and his ordinary design skill Without any special teaching for the purpose of improving the semiconductor device.

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10. Claims 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Wang (US patent 6,621,125) in view of Applicant Admitted Prior Art (fig 4).

REGARDING CLAIM 9

Wang discloses all the invention except for a pairs of bits line, The AAPA (fig 2,fig 3) however, disclose a core cell with pairs of bit line.

It would have been obvious for one of ordinary skill in the art at the time the invention was made to combine the teachings by the AAPA with the teachings by Wang and come up with the invention of claim 9 for the purpose of improving the AAPA semiconductor device as suggested by Wang in his abstract.

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ALLOWABLE SUBJECT MATTER

Claims 10 is objected as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all the limitations of the base claim and any intervening claim.

Claim 10 is considered allowable since prior arts of record fail to teach the structure as recited in claim 10.

- When responding to the office action, Applicants are advised to provide the examiner with the line numbers and the page numbers in the application and/or references cited to assist the examiner to locate the appropriate paragraphs.
- 13. A shortened statutory period for response to this action is set to expire 3 (three) months and 0 (zero) day from the day of this letter. Failure to respond within the period for response will cause the application to be abandoned (see M.P.E.P. 710.02(b)).

CONCLUSION

14. The prior arts made of record and not relied upon are considered pertinent to applicant disclosure: Kim (US patent 6,118,158) disclose a static random access memory having a memory cell array region in which the unit cell is arranged in a matrix.

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15. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thinh T Nguyen whose telephone number is 571-272-1790. The examiner can normally be reached on Monday-Friday 9:00am-6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached at 571-272-1787.

The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Thinh T. Nguyen

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David Nelms
Supervisory Patent Examiner

Technology Center 2800